

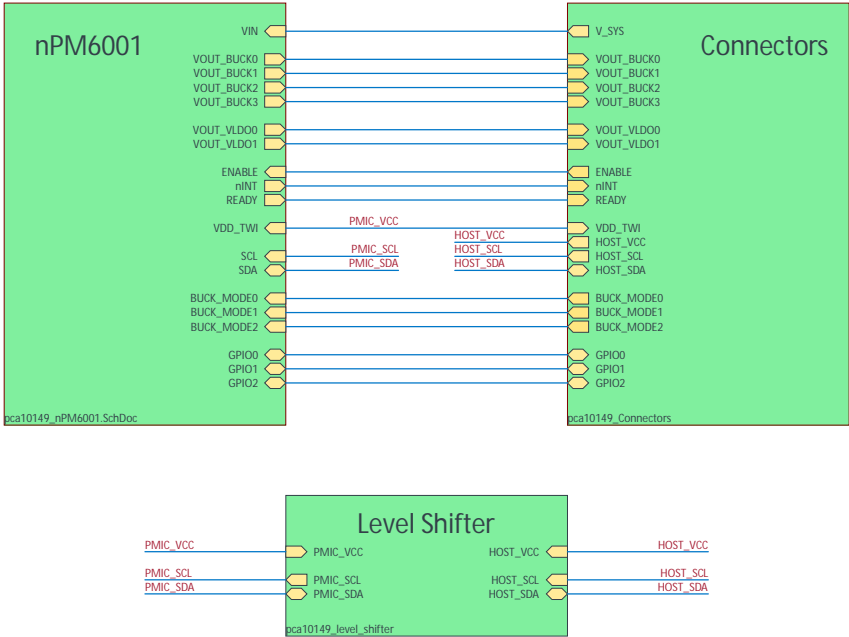
Nordic Semiconductor ASA

nPM6001 PMIC Evaluation Kit (PCA10149)


Sheet 2: nPM6001

Sheet 3: Connectors

Sheet 4: Level shifter



✖ The No ERC object is a design directive.
This directive is placed on a node in the circuit to suppress harmless warnings and/or error violation conditions that are detected when the schematic project is compiled.

Title nPM6001 Evaluation kit			
Size A3	PCB Assembly Number PCA10149	Revision 0.7.0	
Date: 14/09/2022		Sheet 1 of 4	
File: pca10149_Cover_Sheet.SchDoc		Drawn By: KOD1	
Classification: PUBLIC			

A

B

C

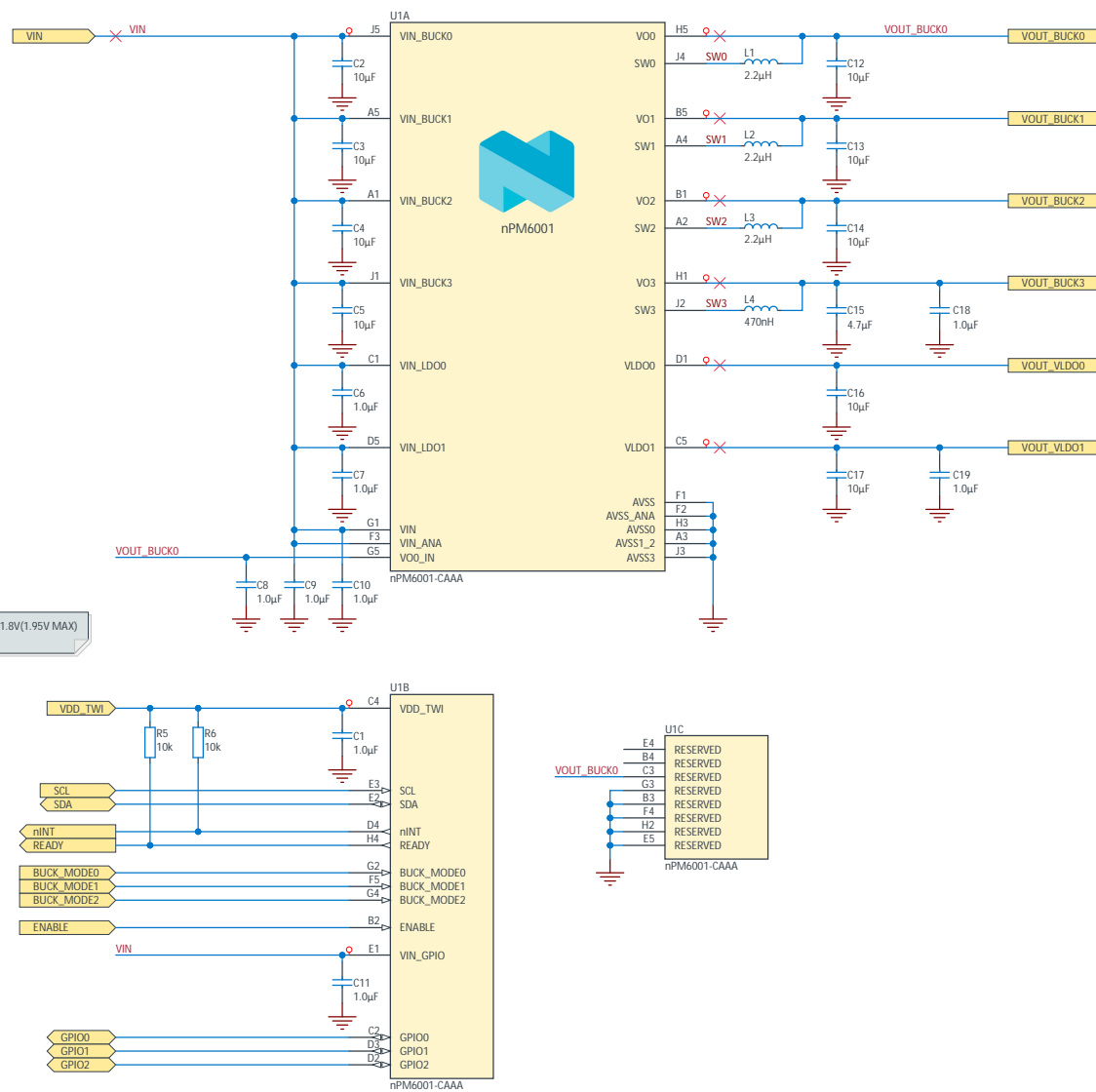
D


A

B

C

D



Title nPM6001 Evaluation kit			
Size A3	PCB Assembly Number PCA10149	Revision 0.7.0	
Date: 14/09/2022		Sheet 2 of 4	
File: pca10149_nPM6001_SchDoc		Drawn By: KOD1	
Classification: PUBLIC			

A

B

C

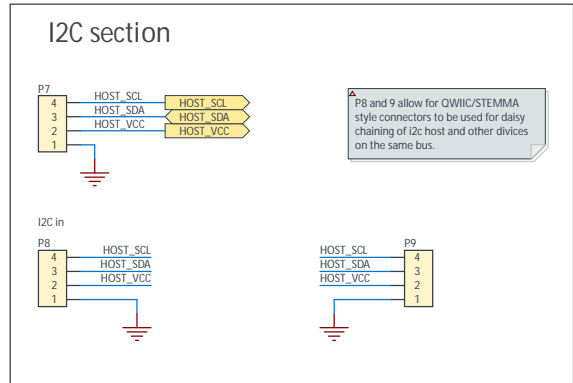
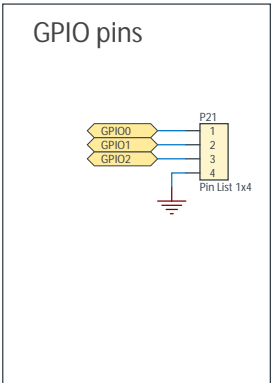
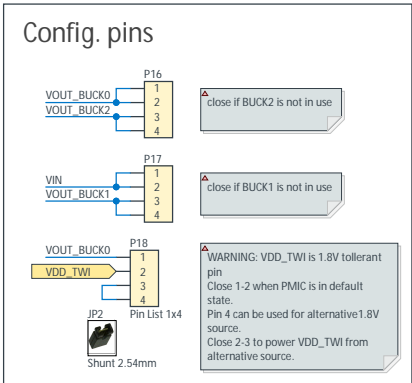
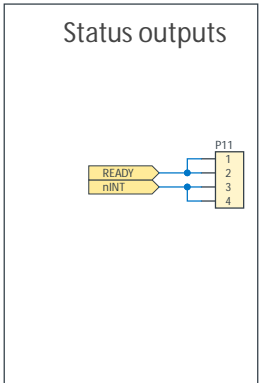
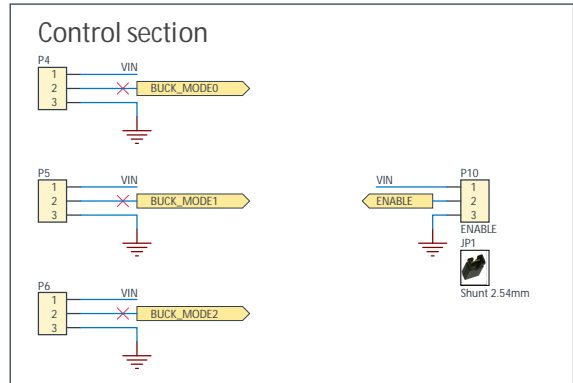
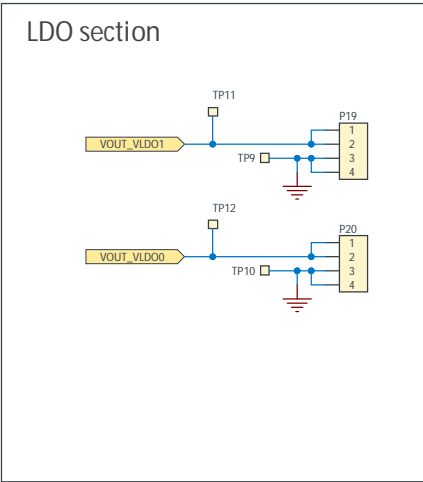
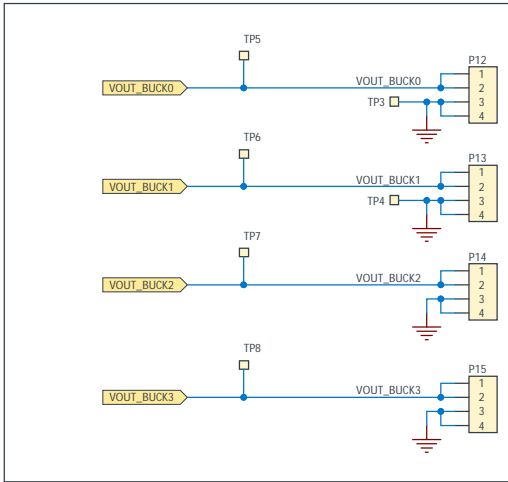
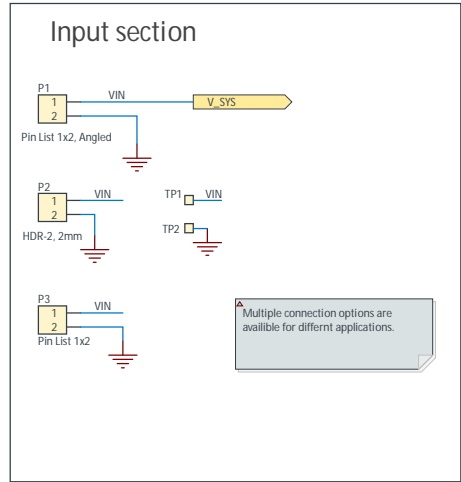
D

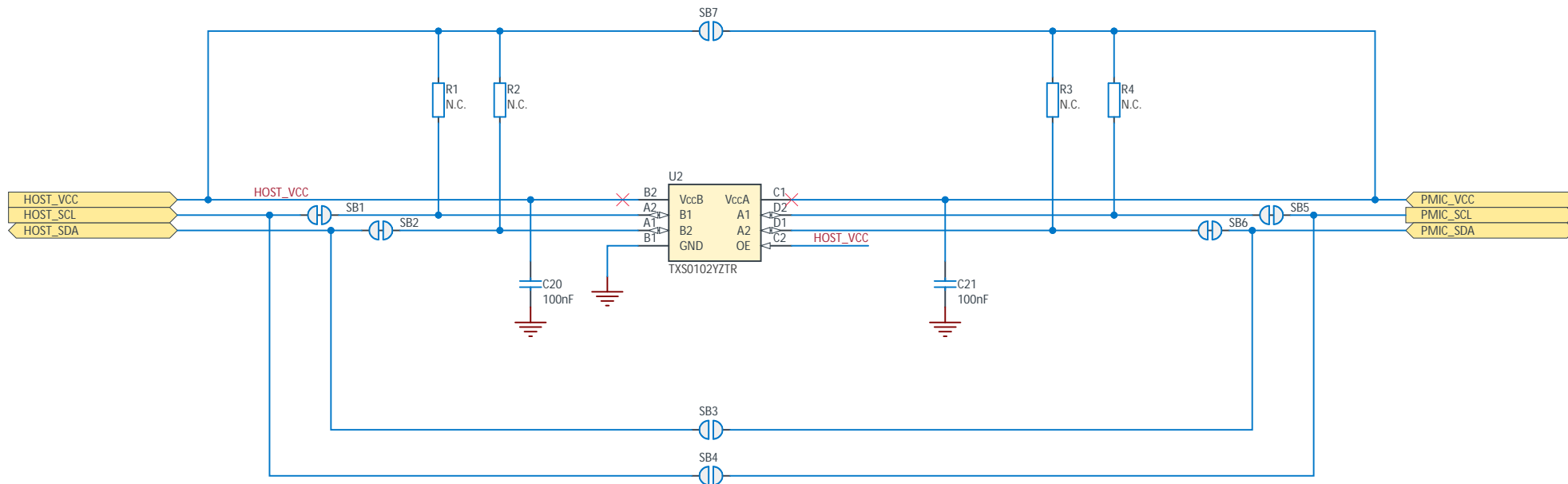
A

B


C

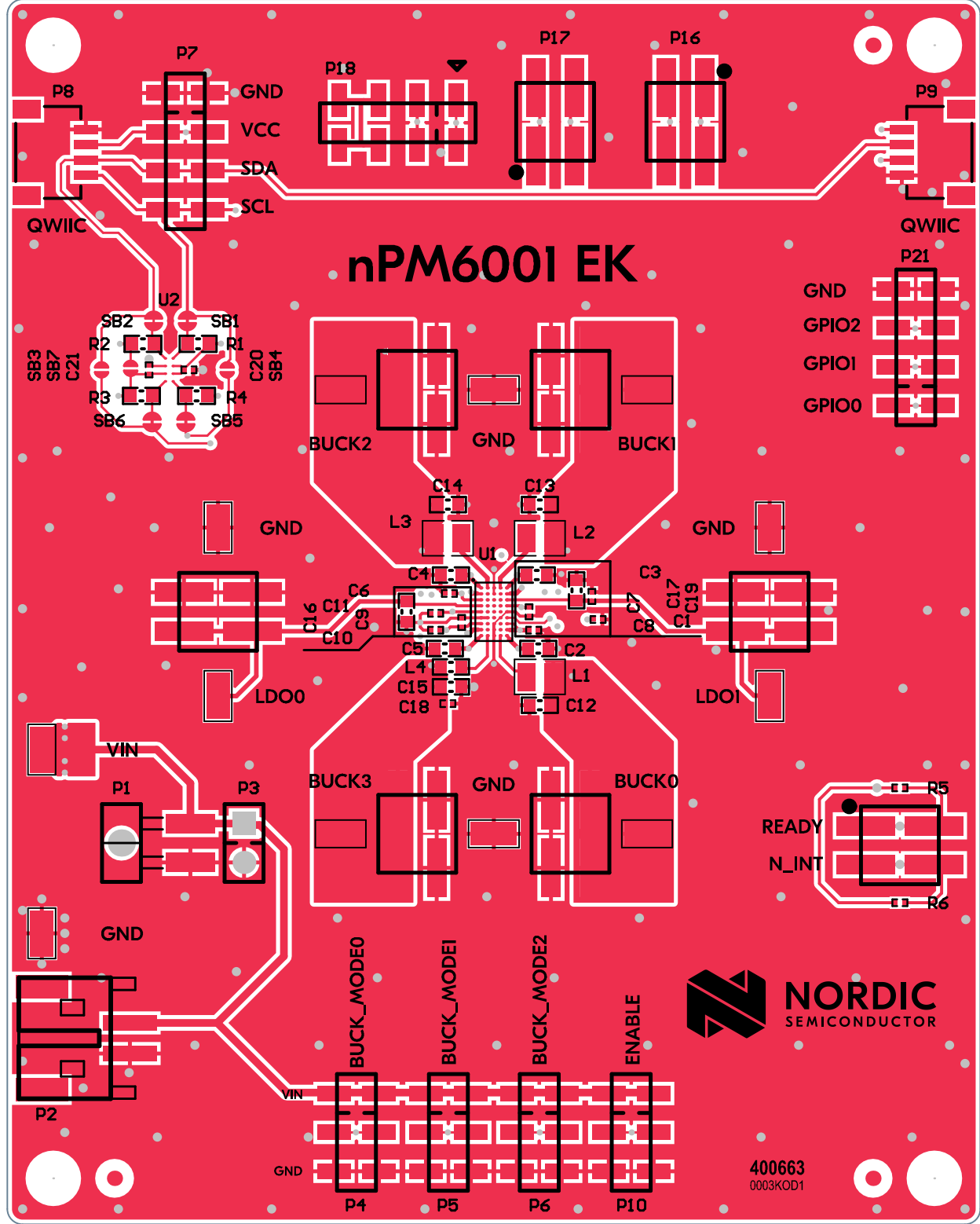
D





For cases where host TWI is at 1.8V level it is possible to bypass the level shifter and have a direct connection.

Title nPM6001 Evaluation kit			
Size A4	PCB Assembly Number PCA10149	Revision 0.7.0	
Date: 14/09/2022		Sheet 4 of 4	
File: pca10149_level_shifter.SchDoc		Drawn By: KOD1	
Classification: PUBLIC			



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